Interference Searched **EAST Search History**

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 7 | (((write or store) near2 (buffer or FIFO or queue)) same (enabl$3 or activ$5) same (inhibit$3 or stop or ceas$3)).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:39 |
| L2 | 1 | ((store adj buffer) and (write adj buffer) and (priority) and capacity).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:42 |
| L3 | 2 | ((write adj buffer) same (store adj buffer)).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:42 |
| L4 | 1 | ((store adj buffer) and (write adj buffer) and (priority) and capacity).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:43 |
| L5 | 1 | ((inhibit$3 or stop or ceas$3) with (priority adj task)).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:43 |
| L6 | 4 | ((write near2 (inhibit$3 or ceas$3 or stop$4 or prevent$3)) with ((write adj buffer) or WB) ).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:43 |
| L7 | 1 | ((write adj buffer) and (store adj buffer) and priority and (interrupt adj handler)).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:44 |
| L8 | 2 | ((store adj buffer) same (write adj buffer) same (write near2 (data or information))).clm. | US-PGPUB | OR | OFF | 2007/02/28 08:44 |

# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 269 | (710/262).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/28 08:34 |
| L2 | 300 | (710/264).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/28 08:34 |
| L3 | 221 | (712/35).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/28 08:34 |
| L4 | 257 | (write adj buffer) and (store adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:34 |
| L5 | 139 | (store adj buffer) and (write adj buffer) and capacity | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:35 |
| L6 | 0 | 1 and 4 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:36 |
| L7 | 0 | 2 and 4 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:35 |
| L8 | 1 | 3 and 4 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:35 |
| L9 | 0 | 1 and 5 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:37 |

# EAST Search History

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|---|---|---|---|---|---|---|
| L10 | 0 | 2 and 5 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:36 |
| L11 | 1 | 3 and 5 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:36 |
| L12 | 492 | ((write or store) near2 (buffer or FIFO or queue)) same (enabl$3 or activ$5) same (inhibit$3 or stop or ceas$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:36 |
| L13 | 3 | 1 and 12 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:37 |
| L14 | 1 | 2 and 12 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:37 |
| L15 | 0 | 3 and 12 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:37 |
| S1 | 492 | ((write or store) near2 (buffer or FIFO or queue)) same (enabl$3 or activ$5) same (inhibit$3 or stop or ceas$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:36 |

# EAST Search History

| | | | | | | |
|---|---|---|---|---|---|---|
| S2 | 43 | ((write or store) near2 (buffer or FIFO or queue)) same (enabl$3 or activ$5) same (inhibit$3 or stop or ceas$3) same priority | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:00 |
| S3 | 41 | S2 and @pd<="20040228" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:14 |
| S4 | 6 | ((write or store) near2 (buffer or FIFO or queue)) same (enabl$3 or activ$5) same (inhibit$3 or stop or ceas$3) same priority same capacity | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:17 |
| S5 | 124 | ((write or store) near2 (buffer or FIFO or queue)) with (write adj information) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:18 |
| S6 | 1 | (((write or store) near2 (buffer or FIFO or queue)) with (write adj information)) same DSP | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:18 |
| S7 | 0 | (write adj buffer) same (store adj bufer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:19 |
| S8 | 65 | (write adj buffer) same (store adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:31 |
| S9 | 567 | (711/158).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/27 15:19 |

# EAST Search History

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|---|---|---|---|---|---|---|
| S10 | 1 | S8 and S9 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:22 |
| S11 | 2281 | priority adj task | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:22 |
| S12 | 1 | (priority adj task) same (write adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:22 |
| S13 | 196 | (write adj buffer) and (store adj buffer) and priority | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:34 |
| S14 | 58 | (write adj buffer) and (store adj buffer) and priority and (interrupt adj handler) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:33 |
| S15 | 4 | ((write adj buffer) near10 (enabl$3 or activ$5)) with (high$2 near2 priority) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 15:34 |
| S16 | 27 | (inhibit$3 or stop or ceas$3) with (priority adj task) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:32 |

# EAST Search History

| | | | | | | |
|---|---|---|---|---|---|---|
| S17 | 1 | (store adj buffer) same (write adj buffer) same (interrupt adj handler) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:33 |
| S18 | 5 | (store adj buffer) same (write adj buffer) same (interrupt) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:38 |
| S19 | 277 | (enabl$3 near10 capacity near10 write) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:37 |
| S20 | 123 | (store adj buffer) and (write adj buffer) and (priority) and capacity | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/28 08:35 |
| S21 | 4 | (store adj buffer) and (write adj buffer) and (priority near2 task) and capacity | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:40 |
| S22 | 2 | (store adj buffer) same (write adj buffer) same (write adj information) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:40 |
| S23 | 46 | (store adj buffer) same (write adj buffer) same (write near2 (data or information)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:50 |

# EAST Search History

| | | | | | | |
|---|---|---|---|---|---|---|
| S24 | 1 | (store adj buffer) same (write adj buffer) same (write near2 (data or information)) same priority | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:45 |
| S25 | 413 | (711/117).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/27 16:45 |
| S26 | 595 | (711/122).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/27 16:46 |
| S27 | 645 | (711/151).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/27 16:46 |
| S28 | 4 | S1 and S25 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:47 |
| S29 | 5 | S1 and S26 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:05 |
| S30 | 5 | S1 and S27 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:49 |
| S31 | 2 | (store adj buffer) same (write adj buffer) same (write near2 (data or information)) same capacity | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:52 |
| S32 | 3 | (store adj buffer) same (write adj buffer) same (write near2 (data or information)) same (enabl$3 or activ$5) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:54 |

# EAST Search History

| | | | | | | |
|---|---|---|---|---|---|---|
| S33 | 3 | (store adj buffer) same ((write adj buffer) or WB) same (write near2 (data or information)) same (enabl$3 or activ$5) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:58 |
| S34 | 0 | (store adj buffer) same ((write adj buffer) or WB) same DSP | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 16:58 |
| S35 | 1 | (store adj buffer) same ((write adj buffer) or WB) same (DSP or (digital adj signal adj processor)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:06 |
| S36 | 46 | (write adj buffer) same (write adj information) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:01 |
| S37 | 0 | (write adj buffer) same (write adj information) same (store adj buffe) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:01 |
| S38 | 2 | (write adj buffer) same (write adj information) same (store adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:02 |
| S39 | 3 | (write adj buffer) same (write near2 information) same (store adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:03 |

# EAST Search History

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|---|---|---|---|---|---|---|
| S40 | 3 | (enabl$3 near2 write near2 buffer) same (store adj buffer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:05 |
| S41 | 653 | (711/152).CCLS. | USPAT; USOCR | OR | OFF | 2007/02/27 17:05 |
| S42 | 1 | S1 and S41 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:05 |
| S43 | 39 | (store adj buffer) and ((write adj buffer) or WB) and (DSP or (digital adj signal adj processor)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:08 |
| S44 | 10 | (write near2 access) with ((write adj buffer) or WB) with priority | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:09 |
| S45 | 1 | (write near2 (inhibit$3 or ceas$3 or stop$4 or prevent$3)) with ((write adj buffer) or WB) with priority | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:10 |
| S46 | 136 | (write near2 (inhibit$3 or ceas$3 or stop$4 or prevent$3)) with ((write adj buffer) or WB) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:10 |
| S47 | 3 | (write near2 (inhibit$3 or ceas$3 or stop$4 or prevent$3)) with ((write adj buffer) or WB) and (store adj (buffer or FIFO or queue)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 17:11 |

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Computers, IEEE Transactions on
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Nguyen, S.; Mitra, S.S.; Reddy, V.; Ganesan, V.; de Lange, W.J.;
Solid-State Circuits, IEEE Journal of
Volume 30, Issue 11, Nov. 1995 Page(s):1227 - 1238
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Garg, A.; Rashid, M.W.; Huang, M.;
Computer Architecture, 2006. 33rd International Symposium on
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Chung, J.W.; Chafi, H.; Minh, C.C.; McDonald, A.; Carlstrom, B.; Kozyrakis, C.
High-Performance Computer Architecture, 2006. The Twelfth International Sym
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Ang, B.S.; Chiou, D.; Rudolph, L.; Arvind;

High Performance Computing, 1998, HIPC '98, 5th International Conference O
17-20 Dec. 1998 Page(s):228 - 237
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Microarchitecture, 1998, MICRO-31, Proceedings, 31st Annual ACM/IEEE Inte
Symposium on
30 Nov.-2 Dec. 1998 Page(s):160 - 169
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1 Evaluation of memory system for integrated Prolog processor IPP

M. Morioka, S. Yamaguchi, T. Bandoh

April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89**, Volume 17 Issue 3

**Publisher:** ACM Press

Full text available: pdf(1.05 MB) Additional Information: full citation, abstract, references, index terms

This paper discusses an optimal memory system to realize a high performance integrated Prolog processor, the IPP. First, the memory access characteristics of Prolog are analyzed by a simulator, which simulates the execution of a Prolog program at a micro instruction level. The main findings from this analysis are that: the write access ratio of Prolog is larger than that of procedural languages; and performance improvement requires the memory system to process concentrated, large write acce ...

2 Integrating reliable memory in databases

Wee Teck Ng, Peter M. Chen

August 1998 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 7 Issue 3

**Publisher:** Springer-Verlag New York, Inc.

Full text available: pdf(123.18 KB) Additional Information: full citation, abstract, index terms

Recent results in the Rio project at the University of Michigan show that it is possible to create an area of main memory that is as safe as disk from operating system crashes. This paper explores how to integrate the reliable memory provided by the Rio file cache into a database system. Prior studies have analyzed the performance benefits of reliable memory; we focus instead on how different designs affect reliability. We propose three designs for integrating reliable memory into databases: non ...

**Keywords**: Main memory database system (MMDB), Recovery, Reliability

3 A microprocessor display controller for combining refresh and storage tube graphics

Steven G. Satterfield, Francisco Rodriguez, David F. Rogers

August 1978 **ACM SIGGRAPH Computer Graphics , Proceedings of the 5th annual conference on Computer graphics and interactive techniques SIGGRAPH '78**, Volume 12 Issue 3

**Publisher:** ACM Press

Full text available: pdf(570.90 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes a stand alone graphics system utilizing a microprocessor based display controller with the capability of combining refresh with storage tube graphics. This combination is accomplished by utilizing the Write-Thru feature of a Tektronix 4014 display terminal. The display controller is a typical Z-80 microprocessor system interfaced to the 4014 by a standard Tektronix parallel interface. A portion of the Z-80 memory is used as the display buffer, allowing it to be divided ...

**Keywords**: Microprocessor display controller, Refresh display, Storage tube display, Vector graphics

4 A frame buffer system with enhanced functionality

F. C. Crow, M. W. Howard

August 1981 **ACM SIGGRAPH Computer Graphics , Proceedings of the 8th annual conference on Computer graphics and interactive techniques SIGGRAPH '81**, Volume 15 Issue 3

**Publisher:** ACM Press

Full text available: pdf(561.14 KB) Additional Information: full citation, abstract, references, index terms

A video-resolution frame buffer system with 32 bits per pixel is described. The system includes, in addition to standard features for limited zoom and pan, an arithmetic unit at the update port which allows local computation of many frequently-used pixel-level functions combining stored pixel values with incoming pixel values. In addition to the standard arithmetic and logical functions there are functions for sum to maximum pixel value and difference to minimum pixel value. Comparisons bet ...

5 Performance comparison of thrashing control policies for concurrent Mergesorts with parallel prefetching

Kun-Lung Wu, Philip S. Yu, James Z. Teng

June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1993 ACM SIGMETRICS conference on Measurement and modeling of computer systems SIGMETRICS '93**, Volume 21 Issue 1

**Publisher:** ACM Press

Full text available: pdf(1.23 MB) Additional Information: full citation, abstract, references, citings, index terms

We study the performance of various run-time thrashing control policies for the merge phase of concurrent mergesorts using parallel prefetching, where initial sorted runs are stored on multiple disks and the final sorted run is written back to another dedicated disk. Parallel prefetching via multiple disks can be attractive in reducing the response times for concurrent mergesorts. However, severe *thrashing* may develop due to imbalances between input and output rates, thus a large number o ...

6 Can message buffers be characterized in linear temporal logic?

A. P. Sistla, E. M. Clarke, N. Francez, Y. Gurevich

August 1982 **Proceedings of the first ACM SIGACT-SIGOPS symposium on Principles of distributed computing PODC '82**

**Publisher:** ACM Press

Full text available: pdf(577.60 KB) Additional Information: full citation, abstract, references, citings, index terms

Exchange of information between executing processes is one of the primary reasons for process interaction. Many distributed systems implement explicit message passing primitives to facilitate intercommunication. Typically, a process executes a write command to pass a message to another process, and the target process accepts the

message by executing a read command. The semantics of write and read may differ considerably dep ...

7 Panel: Extensible database systems

D. S. Batory, M. Mannino

June 1986 **ACM SIGMOD Record , Proceedings of the 1986 ACM SIGMOD international conference on Management of data SIGMOD '86**, Volume 15 Issue 2

**Publisher:** ACM Press

Full text available: pdf(520.69 KB) Additional Information: full citation, abstract, references, citings, index terms

New implementation techniques and new capabilities for database systems are being developed and proposed at a rapid rate. Novel file structures and improved algorithms for query optimization, buffer and recovery management, and transaction management have the potential of realizing significant gains in DBMS performance. The proposed integration of design objects, voice, text, rules, vector graphics, and images into databases promises exciting new capabilities for DBMSs. To accommodate advan ...

8 MU6-G, a new design to achieve mainframe performance from a mini-sized computer

D. B.G. Edwards, A. E. Knowles, J. V. Woods

May 1980 **Proceedings of the 7th annual symposium on Computer Architecture ISCA '80**

**Publisher:** ACM Press

Full text available: pdf(655.13 KB) Additional Information: full citation, abstract, references, citings, index terms

MU6-G is a high performance machine useful for general or scientific applications. Its order code and architecture are designed to be sympathetic to the needs of the operating system and to both the compilation and execution of programs written in high level languages and to support a word size suitable for high precision scientific computations. Advanced technology, coupled with simplicity of design, is used to achieve a high and more readily predictable performance. Innovative features in ...

9 A socket-based manifestation of streams

Marc A. Criley

June 2001 **ACM SIGAda Ada Letters**, Volume XXI Issue 2

**Publisher:** ACM Press

Full text available: pdf(399.36 KB) Additional Information: full citation, abstract

The Ada.Streams package, hereafter referred to simply as the Streams package, introduced into Ada 95 a standard mechanism for the storage and transmission of heterogeneous data within and amongst Ada programs. Section 13.13 of the Ada 95 LRM defines the streams features of the language, specifying the programmatic interface and behavior of this capability. It states that a "stream type may be implemented in various ways, such as an external sequential file, an internal buffer, or network channel ...

10 Garbage collection for a client-server persistent object store

Laurent Amsaleg, Michael J. Franklin, Olivier Gruber

August 1999 **ACM Transactions on Computer Systems (TOCS)**, Volume 17 Issue 3

**Publisher:** ACM Press

Full text available: pdf(267.18 KB) Additional Information: full citation, abstract, references, citings, index terms, review

We describe an efficient server-based algorithm for garbage collecting persistent object stores in a client-server environmnet. The algorithm is incremental and runs concurrently with client transactions. Unlike previous algorithms, it does not hold any transactional locks on data and does non require callbacks to clients. It is fault-tolerant, but performs

very little logging. The algorithm has been designed to be integrated into existing systems, and therefore it works with standard i ...

**Keywords**: client-server system, logging, persistent object-store, recovery

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